



**GP**  
**ELECTRONICS**

**GPM680NP04LQA**

**40V N- and P- Channel MOSFET**

### Product Summary

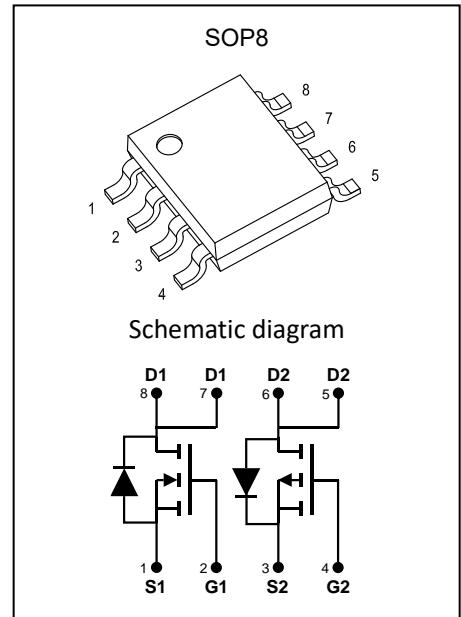
$V_{(BR)DSS}$	$R_{DS(on)TYP}$	$I_D$
40V	26mΩ@10V	6A
	31mΩ@4.5V	
-40V	65mΩ@-10V	-4A
	82mΩ@-4.5V	

### Feature

- Trench Technology Power MOSFET
- Low  $R_{DS(ON)}$
- Low Gate Charge

### Application

- Load Switch
- DC/DC Converter



### MARKING:



### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter		Symbol	Value	Value	Unit
Drain - Source Voltage		$V_{DS}$	40	-40	V
Gate - Source Voltage		$V_{GS}$	$\pm 20$	$\pm 20$	V
Continuous Drain Current <sup>1,6</sup>	$T_A = 25^\circ\text{C}$	$I_D$	6	-4	A
Continuous Drain Current <sup>1,6</sup>	$T_A = 100^\circ\text{C}$	$I_D$	4	3	A
Pulsed Drain Current <sup>2</sup>		$I_{DM}$	24	-16	A
Single Pulsed Avalanche Current <sup>3</sup>		$I_{AS}$	11	-11.5	A
Single Pulsed Avalanche Energy <sup>3</sup>		$E_{AS}$	30	33	mJ
Power Dissipation <sup>5</sup>	$T_A = 25^\circ\text{C}$	$P_D$	2		W
Thermal Resistance from Junction to Ambient <sup>6</sup>		$R_{\theta JA}$	60		°C/W
Junction Temperature		$T_J$	150	150	°C
Storage Temperature		$T_{STG}$	-55~+150	-55~+150	°C

**MOSFET ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$  unless otherwise noted)**
**NMOS:**

Parameter	Symbol	Test Condition	Min	Type	Max	Unit
<b>Off Characteristics</b>						
Drain - Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	40			V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 32\text{V}, V_{\text{GS}} = 0\text{V}$			1	$\mu\text{A}$
Gate - Body Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}} = \pm 20\text{V}, V_{\text{DS}} = 0\text{V}$			$\pm 100$	nA
<b>On Characteristics<sup>3</sup></b>						
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	1	1.6	2.5	V
Drain-source On-resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 5\text{A}$		26	35	$\text{m}\Omega$
		$V_{\text{GS}} = 4.5\text{V}, I_D = 3\text{A}$		31	45	
<b>Dynamic Characteristics</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}} = 20\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		502		pF
Output Capacitance	$C_{\text{oss}}$			46		
Reverse Transfer Capacitance	$C_{\text{rss}}$			34		
Gate Resistance	$R_g$	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		3.5		$\Omega$
<b>Switching Characteristics</b>						
Total Gate Charge	$Q_g$	$V_{\text{DS}} = 20\text{V}, V_{\text{GS}} = 10\text{V}, I_D = 5\text{A}$		11		nC
Gate-source Charge	$Q_{\text{gs}}$			1.4		
Gate-drain Charge	$Q_{\text{gd}}$			2.2		
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 20\text{V}, V_{\text{GS}} = 10\text{V}, I_D = 6\text{A}, R_G = 3\Omega$		8		ns
Turn-on Rise Time	$t_r$			16		
Turn-off Delay Time	$t_{\text{d}(\text{off})}$			27		
Turn-off Fall Time	$t_f$			15		
<b>Source - Drain Diode Characteristics</b>						
Diode Forward Voltage <sup>4</sup>	$V_{\text{SD}}$	$V_{\text{GS}} = 0\text{V}, I_s = 3\text{A}$			1.2	V

**MOSFET ELECTRICAL CHARACTERISTICS (T<sub>J</sub> = 25°C unless otherwise noted)**
**PMOS:**

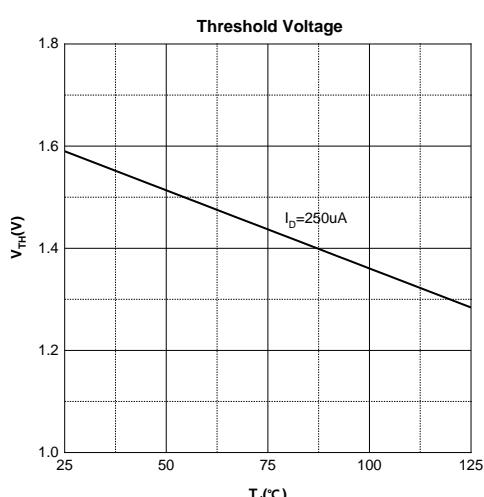
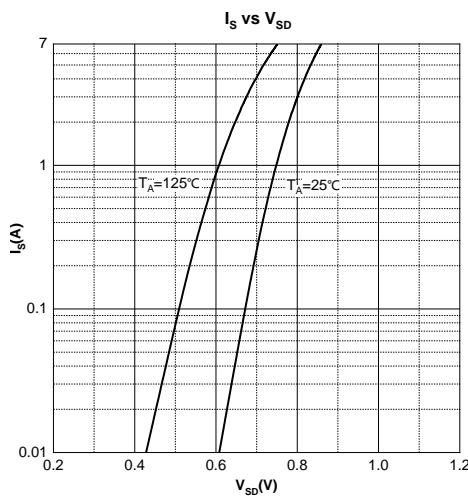
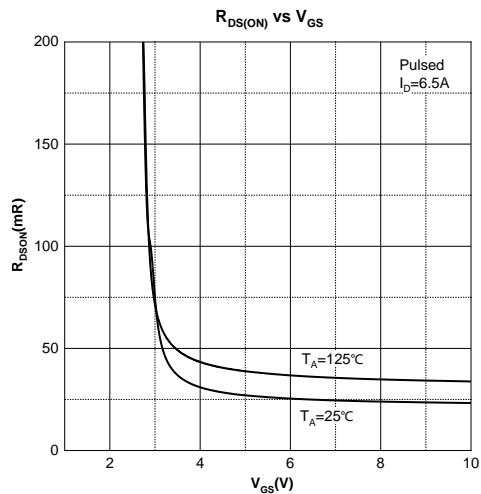
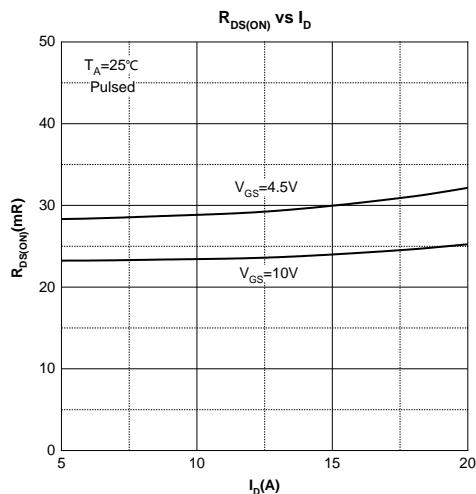
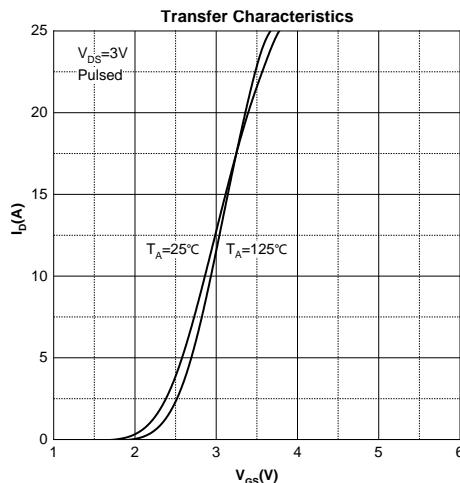
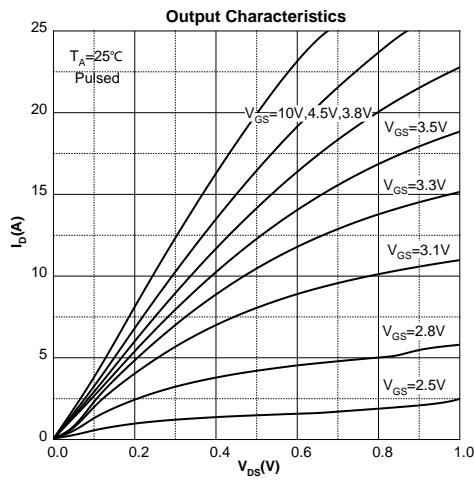
Parameter	Symbol	Test Condition	Min	Type	Max	Unit
<b>Off Characteristics</b>						
Drain - Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250μA	-40			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = -32V, V <sub>GS</sub> = 0V			-1	μA
Gate - Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V			±100	nA
<b>On Characteristics<sup>3</sup></b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250μA	-1	-1.5	-3	V
Drain-source On-resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = -10V, I <sub>D</sub> = -5A		68	88	mΩ
		V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -4A		82	100	
<b>Dynamic Characteristics</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = -20V, V <sub>GS</sub> = 0V, f = 1MHz		619		pF
Output Capacitance	C <sub>oss</sub>			72		
Reverse Transfer Capacitance	C <sub>rss</sub>			51		
Gate Resistance	R <sub>g</sub>	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1MHz		12		Ω
<b>Switching Characteristics</b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = -20V, V <sub>GS</sub> = -10V, I <sub>D</sub> = -5A		12		nC
Gate-source Charge	Q <sub>gs</sub>			1.5		
Gate-drain Charge	Q <sub>gd</sub>			2.8		
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = -15V, V <sub>GS</sub> = -10V, I <sub>D</sub> = -5A, R <sub>G</sub> = 3Ω		21		ns
Turn-on Rise Time	t <sub>r</sub>			15		
Turn-off Delay Time	t <sub>d(off)</sub>			53		
Turn-off Fall Time	t <sub>f</sub>			12		
<b>Source - Drain Diode Characteristics</b>						
Diode Forward Voltage <sup>4</sup>	V <sub>SD</sub>	V <sub>GS</sub> = 0V, I <sub>S</sub> = -4A			1.2	V

Notes :

- 1.The maximum current rating is limited by package.
- 2.Pulse Test : Pulse Width ≤ 10μs, duty cycle ≤ 1%.
- 3.EAS condition: V<sub>DD</sub> = ±20V, V<sub>GS</sub> = ±10V, L = 0.5mH, R<sub>G</sub> = 25Ω Starting T<sub>J</sub> = 25° °C.
- 4.Pulse Test : Pulse Width ≤ 300μs, duty cycle ≤ 2%.
- 5.The power dissipation P<sub>D</sub> is limited by T<sub>J(MAX)</sub> = 150°C.
- 6.Device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with T<sub>A</sub> = 25°C.

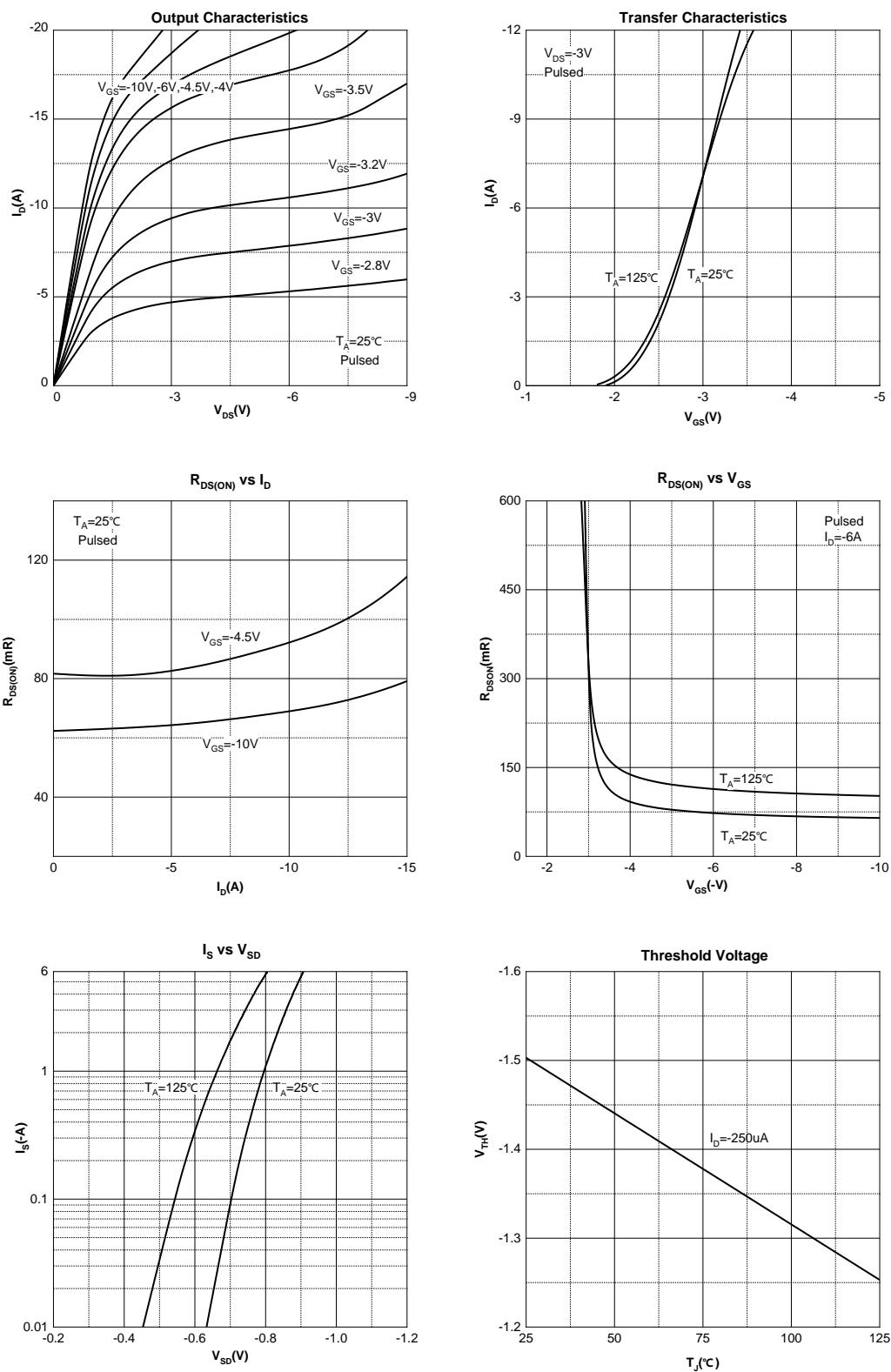
## Typical Characteristics

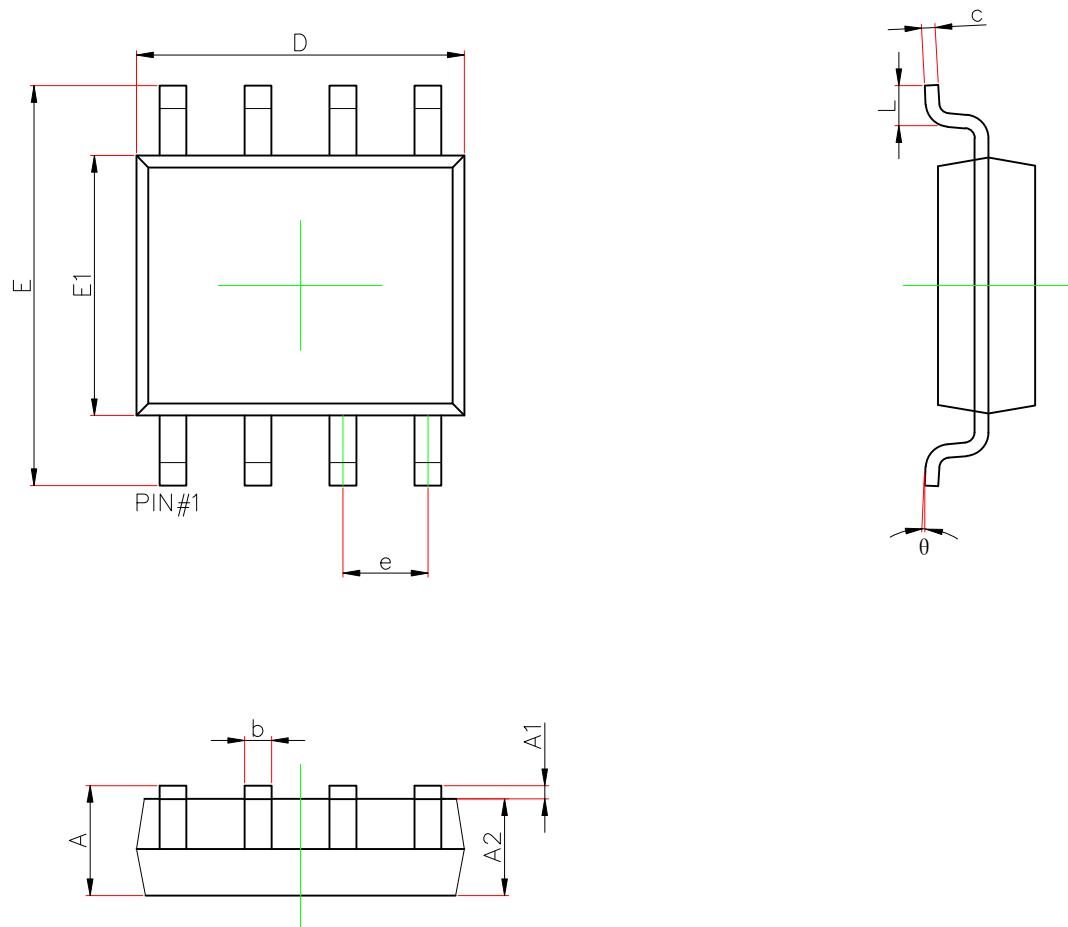
NMOS:



## Typical Characteristics

**PMOS:**



**SOP8 Package Information**


<b>Symbol</b>	<b>Dimensions In Millimeters</b>		<b>Dimensions In Inches</b>	
	<b>Min.</b>	<b>Max.</b>	<b>Min.</b>	<b>Max.</b>
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.156	0.250	0.006	0.010
D	4.700	5.100	0.185	0.201
e	1.270(BSC)		0.050(BSC)	
E	5.800	6.200	0.228	0.244
E1	3.700	4.100	0.146	0.161
L	0.400	1.270	0.016	0.05
θ	0°	8°	0°	8°